

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Hyang-Ja Yang, *et al.*
U.S. Serial No.: 10/602,111
Filing Date: June 23, 2003
Examiner: Nguyen, Dang T.
Group Art Unit: 2824
Date of Notice of Allowance: February 28, 2005
Title: SEMICONDUCTOR MEMORY DEVICE WITH DATA
INPUT/OUTPUT ORGANIZATION IN MULTIPLES
OF NINE BITS

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the United States Post Office as First Class Mail on the date indicated below in an envelope addressed to Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

5/10/05

Date

Chelsey Davis

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL LETTER

Sir:

Enclosed herewith for filing in the above-identified patent application please find the following:

1. Fee(s) Transmittal - PTOL-85; ;
2. Check in the amount of \$1,730.00 to cover Issue Fee, ten (10) advance-order copies of the printed patent and the Publication Fee;
3. Transmittal of Formal Drawings;
4. Thirteen (13) Sheets of Formal Drawings (Figures 1-3, 4A-4C and 5-11); and
5. Return Postcard

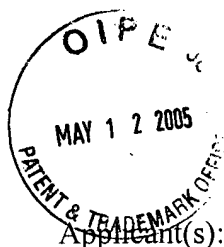
In connection with the foregoing matter, please charge any additional fees which may be due, or credit any overpayment, to Deposit Account Number 50-1798. A duplicate copy of this letter is provided for this purpose.

Respectfully submitted,

Date: 5/10/05
Mills & Onello, LLP
Eleven Beacon Street, Suite 605
Boston, MA 02108
Telephone: (617) 994-4900
Facsimile: (617) 742-7774

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Steven M. Mills
Registration Number 36,610
Attorney for Applicant



PATENT
Attorney Docket No.: SAM-0392

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TRANSMITTAL OF FORMAL DRAWINGS

Sir:

In response to the Notice of Allowability mailed in the above referenced patent application on February 28, 2005, enclosed herewith are formal Figures 1-3, 4A-4C and 5-11 on thirteen (13) sheets. The enclosed sheets of formal drawings replace the original sheets of Figures 1-3, 4A-4C and 5-11.

Acceptance of the formal drawings is respectfully requested.

Respectfully submitted,

Date: 5/10/05
Mills & Onello, LLP
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Boston, MA 02108
Telephone: (617) 994-4900
Facsimile: (617) 742-7774

J:\SAM\0392\lfee\transforndraw.wpd

Steven M. Mills
Registration Number 36,610
Attorney for Applicants